

FACULTY PROFILE

Name of the Faculty Member : **Dr.K.PRIYADARSHINI**

Present Designation : **PROFESSOR**

Email : **kpriyaanilesh@gmail.com**

I. Particulars of Educational Qualification:

1. Studied B.E - ECE at **V.L.B Janakiammal College of Engineering & Technology Bharathiyar University, 1993 - 1997 .**
2. Studied M.E - **Communication Systems** at **Jayaram College of Engineering & Technology , Anna University, 2006 - 2009.**
3. Studied M.B.A – **Marketing Management** at **IGNOU , 2006.**
4. Ph.D in **Information and communication Engineering** at **Karunya University, 2010-2017.**

II. Title of Ph.D. Thesis

“Analysis and Optimization of Motion Estimation Algorithms With High Performance VLSI Architecture for A Single Frame”

III. Academic Experience:

Name of the College	Designation	Joining Date	Relieving Date	Experience		
				Years	Months	Days
DR.NNCE,THOLUDHUR	Lecturer	09.07.1997	20.12.1997		05	11
ANJALAI AMMAL ENGINEERING COLLEGE, KOVILVENNI, THANJAVUR.	Lecturer	22.12.1997	01.06.1998		05	10
SHRI ANGALAMMAN COLLEGE OF	Sr.Lecturer	02.07.1998	19.09.2006	08		02

ENGINEERING & TECHNOLOGY ,TRICHY.						
PAVENDHAR BHARADHIDASAN COLLEGE OF ENGG.& TECHNOLOGY,TRICHY.	Asst.Professor	20.09.2006	26.09.2011	05		06
TRICHY ENGINEERING COLLEGE, TRICHY.	Asso.Professor	27.09.2011	27.06.2017	05		10
TRICHY ENGINEERING COLLEGE, TRICHY.	Professor	28.06.2017	03.06.2018		11	03
K.RAMAKRISHANAN COLLEGE OF ENGINEERING, SAMAYAPURAM, TRICHY	Professor	04-06-2018	Till Date	01	00	00
Total				21	09	12

IV. List of Publications :

I. INTERNATIONAL JOURNALS

1. D.JackulineMoni, **K. Priyadarshini** “ MultiFrame fast search motion estimation and VLSI Architecture”, International Journal of Scientific and Research Publications 2(7),1-5,2012.
2. **K.Priyadarshini** , D.JackulineMoni “Power consumption and memory Aware VLSI Architecture for motion estimation “ International Journal of Advanced Computer Technology 2(3) ,48-52, 2013.
3. **K.Priyadarshini** , D.JackulineMoni “A Novel approach to improved Unsymmetrical –Cross Multi –Hexagon grid Search algorithm” International Journal of Applied Engineering Research 9(24), 29141-29153,2014. (**indexed by Scopus**)
4. **K.Priyadarshini** ,D.JackulineMoni “A Novel Hybrid Differential Evolution Adaptive approach for block matching in Motion estimation” Middle-East Journal of Scientific Research 23 (12), 2905-2912, 2015. (**Annexure-II**)
5. **K.Priyadarshini** , D.JackulineMoni “Analysis of Block Matching Algorithm

based on Particle Swarm Optimization and Differential Evolution”
International Journal of Applied Engineering Research 11(3), 2055-2058, 2016.
(indexed by Scopus)

6. **K.Priyadarshini** , D.JackulineMoni , “A High Performance VLSI Architecture with Half Pel and Quarter Pel Interpolation for a single frame” “International Journal of Control Theory and Applications, 9(34), 673-680,2016 (indexed by Scopus)
7. **K.Priyadarshini** , T.Muruganantham “A Smart and Efficient Knuckle Pattern Identification for Security Purpose ” International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering 8(4), 1151-1157, 2019.
8. K.Mohana Chandricka, **K.Priyadarshini** , “ Uncompressed Video Streaming In Wireless Channel Without Interpolation Using Search Algorithms” International Journal of Advanced Scientific Research & Development 06 (03), 61-67, 2019.

II NATIONAL & INTERNATIONAL CONFERENCES :

S.No	Name of the Conference & Place	Presented / Participated/ Session Judge	Date & Year
1.	National Conference on Recent Trends in Communication and Signal Processing “ Karunya University ”, Coimbatore.	Presented Paper	13 th March 2009
2.	National Conference on Emerging Trends in Communication “ Jayaram College of Engineering & Technology, Trichy. ”	Presented Paper	8 th May 2009
3.	International conference on Newer Engineering concepts & Technology “ K.Ramakrishnan college of Technology ”, Trichy.	Participated	29 th March 2017
4.	National Conference on Advanced computing Technologies “ DhanaLakshmi Srinivasan Engineering College ” Perambalur.	Session Judge	21 st March 2018
5.	International Conference on Intelligent Computing and Sustainable System (ICICSS’2018) “ Akshaya College of Engg.& Technology,Coimbatore. ”	Presented	21 st September 2018

6.	International Conference on Communication and Electronics System (ICCES '2018) “ PPG Institute of Technology, Technology, Coimbatore. ”	Presented	15 th October 2018
7.	International Conference on Innovative Engineering Initiatives (ICIEI 2019) “ SRM TRP Engineering College ”	Presented	13 th March 2019
8.	National Conference on Emerging Trends In Electronics and Communication Engineering “ K.Ramakrishnan College of Technology ”	Presented	15 th March 2019
9.	National Conference on Emerging Trends in Electronics and Communication Engineering (NCETECE'19) “ Kings college of Engineering ”	Presented	15 th March 2019
10.	International Conference on Intelligent Computing and Control Systems (ICICCS 2019) “ Vaigai College Of Engineering ”	Presented	17 th May 2019

V PATENTS FILED

1. A SMART AND EFFICIENT SECURED DEVICE USING KNUCKKLE PATTERN REGISTRATION

Application No. & Date: 201941014573 , 11/04/2019