

Dr. JENCY RUBIA J



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CAREER OBJECTIVE

To secure a challenging position in a reputed organization where I can use my skills and knowledge to the best.

PROFESSIONAL EXPERIENCE

| S.No | Institution/University | Designation | Duration |
|------|---|---------------------|----------------------|
| 1 | K Ramakrishnan College of Engineering | Assistant Professor | Mar 2022 to Till |
| 2 | M.A.M College of Engineering and Technology | Assistant Professor | Aug 2020 to Feb 2022 |

ACADEMIC PROFILE

| Course | Institution | Year of Passing | Percentage/CGPA |
|----------------------|--|-----------------|-----------------|
| PhD | Sri Venkateswara College of Engineering, Anna University | December 2021 | 8.31 |
| M.E (VLSI Design) | Vel Tech Multi Tech Dr.Rangarajan Dr.Sakunthala Engineering College, Chennai. | May 2015 | 7.97 |
| B.E (ECE) | Karunya University, Coimbatore | April 2013 | 8.01 |
| HSC | Sarah Tucker Hr Sec School, Palayamkottai | March 2009 | 88.6% |
| SSLC | Sarah Tucker Hr Sec School, Palayamkottai | March 2007 | 90.2% |

TECHNICAL DESCRIPTION

Areas of Interest:

- Low Power VLSI Design
- Signal/ Image Processing
- Digital Circuits
- Number Theory for Computer Arithmetic
- Device Modeling
- Modular Arithmetic (RNS,LNS & RLNS)
- Excellent mentoring ability to determine the student's strengths and weaknesses
- Develop novel and original ideas regarding the research and development
- Continuous learner since teaching is a lifelong process.
- Good presentation skills so that listeners can understand.

TECHNICAL SKILLS

Tools

Cadence, H-SPICE, Tanner EDA, Xilinx ISE, Xilinx
VIVADO, MATLAB, TCAD

Languages

VHDL and Verilog, Visual Basic

SPECIAL INTEREST**Technical Interest**

Particle Physics, Quantum Mechanics, Mysteries about Black hole and Big bang theory

Hobbies

Listening music, Embroidery, learning Korean and Hindi

PROFESSIONAL SYNOPSIS**Professional Involvement****Training Involvement****Professional Responsibility**

| | | | | | |
|------------------|---|----------------------------|------|-----------------------------------|---|
| Publication (13) | | Webinar (Organized) | | Editorial Board Member | |
| SCI/WOS | 3 | FDP/FRD | 16 | Peer-Reviewer | 1 |
| SCOPUS | 4 | Delivered Invited Lectures | 13/4 | International Technical Committee | 3 |
| UGC | 6 | Workshop | 1 | CLUB | 10 |
| Conference (12) | | Webinar (Attended) | | COUNCIL | |
| Book Chapters | 3 | Training Program | 9 | Member of Technical Societies | Toastmasters International i-COMM Technovation Club IEEE Council on RFID, IEEE Nanotechnology Council ACM Digital Library, ISTE, IETE, IAENG, Internet Society, UACEE- IRED, SCIEI, SDIWC, IAOE |

PROJECT GRANTS RECEIVED

| S.No | Title of the Project | Funding Agency | Year | Fund | Status |
|------|--|------------------------------|------|----------|-----------|
| 1 | Detection and Spread Avoidance of Corona Virus Using IoT Smart Door System | MASTeR Group of Institutions | 2020 | Rs. 5000 | Completed |

PATENT

| S.No | Title | Filled/Patent No | Filled/Patent Date | Type | Status |
|------|--|------------------|--------------------|-------|--------------|
| 1 | A Novel Implementation of RLNS on Fixed-Width Multiplier | 202041020966 | 05-Jun-2020 | India | FER Received |
| 2 | An IoT Based Door System to Prevent Covid-19 Virus | 202141038100 | 24-Sep-2021 | India | Published |

AWARDS / ACHIEVEMENTS

- Winning First prize** award in the InnoWAH Competition organized by the IIT PALS Chennai in the title "Detection and Spread Avoidance of Corona Virus using IoT based Smart Door System"
- Best Paper Award** in AICTE Sponsored International Conference SMETI 2020
- International Young Researcher Award** by the International Institute of organized Research (I2OR) council which is a registered MSME, Government of India.
- Winning Guinness world record event for taking an online computer programming lesson 24 hours on 24-04-2021 and 25-04-2021.
- Elite Award** in NPTEL Online Course (Digital Circuits)

ACADEMIC EXPERIENCE

Courses Taught

- VLSI Design
- Digital Principles and System Design
- CMOS Digital VLSI Design
- Linear Integrated Circuits and Applications

Lab Courses Taught

- Digital Electronics Laboratory
- VLSI Design Laboratory
- Digital Principles and System Design Laboratory
- Linear and Digital Integrated Circuits Laboratory

POSITION OF RESPONSIBILITY

- Class coordinator (2018 Batch)
- Department Event Coordinator
- Exam cell member
- Session Chair – IEEE 2nd INCET
- IIT Pals InnoWAH coordinator
- VLSI Design Lab In charge (UG & PG)
- KEMT (conducted by Karunya University) worked as a volunteer in managing the event.

PERSONAL PROFILE

| | | |
|-----------------|----------------------|-------------------------------------|
| Name | Jency Rubia J | Permanent Address |
| Father's Name | B Jebaraj Gnanaswamy | W/o Mr. Bibin Lawrence R, |
| Date of Birth | 15 March 1992 | 11-7A, Avaru Vilai, Vencode post, |
| Gender | Female | Kanniyakumari, Tamilnadu – 629 171. |
| Languages known | Tamil and English | |
| Nationality | Indian | |

REFERENCES

Dr. G. A. Sathish Kumar,

Professor,
Department of ECE,
Sri Venkateswara College of Engineering,
Sriperumbudur.
Mail ID: sathish@svce.ac.in
Mob: 9445265296

Dr. Prabhu.V,

Associate Professor & HOD,
Department of ECE,
Vel Tech Multi Tech Dr.Rangarajan Dr.Sakunthala
Engineering College, Chennai
Mail.ID: prabhuv@veltechmultitech.org
Mob: 9840110712

RESEARCH PROFILE

PhD **Title:** Development of a Novel Method For High Speed Parallel Multiplier - Accumulator (MAC)

Abstract: A novel algorithm is proposed to design and development of the Fixed-width multiplier and MAC unit using Residue Logarithmic Number System (RLNS) for VLSI signal processing applications.

Grant Project **Title:** IoT Based Smart Door System to prevent COVID-19 Virus

Abstract: This project proposes a solution for avoiding suspicious persons with Corona symptoms based on an IoT system.

PG Project **Title:** Analysis, Design and Implementation of 4-Bit Full Adder using FinFET

Abstract: To write a HSPICE program for attaining lesser consumption in delay and power of the ALU circuit. FinFET technology (45 nm) was used for achieving the above objectives by means of Synopsys Hspice tool.

Sub-Project 1 **Aim:** To design ALU circuit using Encounter digital flow (Cadence Virtuoso- Encounter to GDSII).

Abstract: The Encounter digital flow allows to route the ALU circuit, such as local routing, area routing, channel routing and global routing. Encounter flow used for to determine time design summary, clock tree synthesizer, LVS and DRC. To create path for power rings, floor planning will be done.

Sub-Project 2 **Aim:** To design 4×1 Multiplexer circuit using Encounter digital flow (Cadence Virtuoso- Encounter to GDSII).

Abstract: The Encounter digital flow allows to route 4×1 Multiplexer circuit, such as local routing, area routing, channel routing and global routing. To determine time design summary, clock tree synthesizer, LVS and DRC encounter flow used. To create path for power rings, floor planning will be done.

UG Project **Title:** Investigation of Nano-scale FinFET for Memories

Abstract: To analyze the performance of FinFET by using different types of materials in the gate. The device modeling is done with the help of Synopsys TCAD Software.

PUBLICATION(13)

SCI/WOS (3)

- 1) Jency Rubia and Sathish Kumar, “ A High Speed Fixed-Width Floating Point Multiplier Using RLNS Algorithm”, International Journal of Electrical Engineering Education, Dec 2018, pp1-9 <https://doi.org/10.1177/0020720918813836>
- 2) Jency Rubia and Sathish Kumar, “FIR Filter Design Using Floating Point Radix 4 Algorithm,” International Journal of Electrical Engineering, Nov 2019, pp1-13 <https://doi.org/10.1177/0020720919891064>
- 3) Jency Rubia J, Salim A, Afindas A and Naveenkumar S, “Diagnosis of COVID-19 using ADAM Optimization technique in Convolutional Neural Network (CNN),” Tierärztliche Praxis, Vol 41, 2021, PP 415-425

SCOPUS (4)

- 1) Jency Rubia and Bibin Lawrence, “Analysis, Design and Implementation of 4 bit Full Adder Using FinFET” , Journal of Convergence Information Technology, vol. 10, no. 2, March 2015, pp. 71-77
- 2) Jency Rubia and Sathish Kumar, “FIR Filter Design Using Floating point Column Bypassing Technique,” International Journal of Recent Technology and Engineering (IJRTE), Volume-8 Issue-2S4, July 2019. <https://doi.org/10.35940/ijrte.B1079.0782S419>.
- 3) Jency Rubia and Babitha Lincy, “Design of Low power 4 bit ALU using 32nm FinFET technology” , International Journal Of Pure And Applied Mathematics, vol. 120, no. 6, July 2018, pp. 8089-8099.
- 4) Jency Rubia and Bibin Lawrence, “Investigation of 6T SRAM Characteristics Using TFET”, International Journal of Engineering and Technology, vol. 7, no. 3.34, 2018 pp. 981-983.

UGC (6)

- 1) Jency Rubia J, Babitha Lincy R and Ahmed Thair Al-Heety, "Moving Vehicle Detection From Video Sequences For Traffic Surveillance System," ITEGAM-JETIA, Vol 7 No 27, PP 41-48, Jan/Feb 2021
- 2) Jency Rubia, Babitha Lincy and Muthu pandian, "Enhancing the Performance of Convolutional Neural Network with Residue Number System" published in the fourth Annual Technical Volume of Electronics and Telecommunication Engineering Division of The Institution of Engineers (India) on the theme "Communication Networking using Intelligent Sensor Technology"
- 3) Jency Rubia and Sathish Kumar, "Design And Analysis Of Low Power Fixed-Width Multiplier Using Reduced Precision Redundancy Block (RPR)" IOSR Journal of VLSI and Signal Processing, vol. 9, no. 6, 2019, pp. 01-08 <https://doi.org/10.9790/4200-0906010108>.
- 4) Jency Rubia and Bibin Lawrence, "Review of Fin FET Technology and Circuit Design Challenges", International Journal of Engineering Research and Applications, vol. 5, no. 12, December 2015, pp. 1-4.
- 5) Jency Rubia and Bibin Lawrence, "Enhanced Modified Booth Recoding Technique for Signal Processing Application", International Journal of Trends in Computational Engineering and Technology, Special Issue 2017.
- 6) Jency Rubia and Bibin Lawrence, "Analyzing the Performance of NoC Using Hierarchical Routing Methodology", International Journal of Emerging Technology in Computer Science and Electronics vol. 13, no. 4, March 2015, pp. 248-255.

International Conference (12)

- 1) Springer International Conference: Springer International Conference on Image Processing and Capsule Networks (ICIPCN 2020), Riverside Hotel, Bangkok, Thailand.
Paper Title: Digital Image Restoration Using Modified Richardson-Lucy Deconvolution Algorithm
- 2) IEEE International Conference: IEEE International Conference for Intelligent Technologies - CONIT 2021
Paper Title: IoT Based Smart Door System to prevent COVID-19 Virus
- 3) IEEE International Conference: 4th International Conference on Signal Processing, Communications and Networking (ICSCN -2017)
Paper Title: A Survey Paper on Modern Technologies in Fixed-Width Multiplier
- 4) IEEE Sponsored International Conference: International conference on Engineering and Technology
Paper Title: Performance Investigation of a Full Adder using CNFET Technology
- 5) AICTE Sponsored International Conference: Online International conference on Smart Materials And Emerging Technologies For Industry 4.0 (SMETI 2020)
Paper Title: Design and Analysis of Digital FIR Filter Using RLNS Fixed Width Multiplier
- 6) International Conference: Virtual Conclave on Computer Vision Impelled Technology - VCCVIT2020
Paper Title: Design and Analysis of QCA Adder Circuits for Efficient Computation
- 7) International Conference: International Conference on Advances in Emerging Technologies.
Paper Title: Analyzing the Performance of NoC Using Hierarchical Routing Methodology
- 8) International Conference: International Conference on Emerging Trends In Information & Communication Technologies.
Paper Title: Error Detection and Correction Using TURBO CODE with Interleaving in FEC Channel
- 9) International Conference: International Conference on Trends in Engineering with Computation Hikes
Paper Title: Enhanced Modified Booth Recoding Technique for Signal Processing Application
- 10) International Conference: International conference on Innovative Techniques in Science, Engineering & Information Technology
Paper Title: Design of Low power 4 Bit ALU Using 32 nm FinFET Technology.

11) International Conference: SITAMS International Conference on Innovations in Engineering, Technology and Science

Paper Title: Investigation of 6T SRAM Characteristics Using TFET

12) International Conference: 2nd International Conference on Recent Innovations in Science, Engineering & Technology

Paper Title: FIR Filter Design Using Floating Point Column Bypassing Technique

Book Chapters (3)

1) Jency Rubia J., Babitha Lincy R. "Digital Image Restoration Using Modified Richardson-Lucy Deconvolution Algorithm," Image Processing and Capsule Networks. Advances in Intelligent Systems and Computing, Vol 1200. PP 100-112 Springer, Cham. https://doi.org/10.1007/978-3-030-51859-2_10

2) Jency Rubia J, Babitha Lincy R, "Design and Analysis of QCA Adder Circuits for Efficient Computation," Annals of Computer Vision Impelled Technology 2020, PP 46-56, Feb 2021.

3) Jency Rubia J, Babitha Lincy R, "Detection of Plant Leaf Diseases using Recent Progress in Deep Learning-Based Identification Techniques," Annals of Computer Vision Impelled Technology 2020, PP 57-65, Feb 2021

TRAINING INVOLVEMENT

FDP (13)

1) Participated and successfully completed 1 week ATAL FDP on "Nano Electronics and RF Engineering" from 06/09/2021 to 10/09/2021 at SRM Institute of Management and Technology.

2) Participated and successfully completed 1 week ATAL FDP on "ASIC DESIGN FOR DRIVING DIGITAL INNOVATIONS IN NEXT GEN PLATFORM" from 23/08/2021 to 27/08/2021 at Acharya Institute of Technology.

3) Participated and successfully completed 1 week ATAL FDP on " VLSI Design, Verification and Testing" from 2021-06-07 to 2021-06-11 at Government Engineering College, Rajkot.

4) Participated and successfully completed 1 week ATAL FDP on " Wireless Embedded Systems and Sensor Networks" from 2021-06-01 to 2021-06-05 at G H Patel College of Engineering & Technology.

5) Participated and successfully completed 1 week ATAL FDP on "Sensors Technology" from 2021-2-15 to 2021-2-19 at Mizoram University.

6) Participated and successfully completed 1 week ATAL FDP on "Photonics" from 2021-2-8 to 2021-2-12 at KKR & KSR INSTITUTE OF TECHNOLOGY AND SCIENCES

7) Participated and successfully completed 1 week ATAL FDP on "Recent Trends and Applications in Biomedical Signal and Image Processing" from 21-09-2020 to 25-09-2020 at G.H. Raisonni College of Engineering, Nagpur.

8) Participated and successfully completed 1 week ATAL FDP on "Quantum Computing" from 19th Oct-23rd October 2020 at Sri Ramakrishna Engineering College, Coimbatore.

9) Participated and successfully completed FDP on "Quantum Computing" from 24th Aug-29rd August 2020 organized by the Electronics & ICT Academies.

10) FDP on Advanced Nano-Scale Device Design Using TCAD, VIT Chennai.

11) Contributed TEQIP-II sponsored One-week Faculty Development Programme on "Research Perspectives of Signal Processing" organized by the department of ECE, PSG College of Technology, Coimbatore during 5-11, August 2016.

12) Participated two days FDP on "Future Research Issue in Computing Technologies" organized by Vel Tech Dr.RR &Dr.SR University held on 9th and 10th December 2016.

13) Participated and successfully completed 1 week ATAL FDP on "Enhancement and reinforcement of moral ethics to the learned faculty members" from 30-11-2020 to 05-12-2020 at M.A.M. College of Engineering and Technology, Tiruchirappalli.

FRD (4)

- 1) Presented Seminar topic entitled “Fixed Width Parallel Multiplier-Accumulator based on Higher Radix Algorithm” in the Faculty Research Day held on 17th April, 2015 at Sri Venkateswara College of Engineering.
- 2) Presented Seminar topic entitled “Fixed Width Parallel Multiplier-Accumulator based on Higher Radix Algorithm” in the Faculty Research Day held on 28th April, 2016 at Sri Venkateswara College of Engineering.
- 3) Presented Seminar topic entitled “Fixed Width Parallel Multiplier-Accumulator based on Higher Radix Algorithm” in the Faculty Research Day held on 26th April, 2017 at Sri Venkateswara College of Engineering.
- 4) Presented Seminar topic entitled “Fixed Width Parallel Multiplier-Accumulator based on Higher Radix Algorithm” in the Faculty Research Day held on 26th April, 2018 at Sri Venkateswara College of Engineering.

Workshop (20)

- 1) Participated Three days’ workshop on “Advanced Digital Design Flow using Cadence” conducted by the Electronics department, MIT Campus, Anna University during March 1-3, 2018.
- 2) Participated two days’ workshop on “System Design Using Xilinx Vivado Design Suit and Zynq-7000 SoC” organized by ECE Department, Anna University (CEG Campus) on 16-17 March 2018.
- 3) Participated in the one day workshop on “Academic Writing in Refereed Journals and Emerging Trends in Research in English Literature & English Language Teaching” organized by the Division of social sciences and Languages, VIT Chennai.
- 4) Participated one-day workshop on “Cyber Awareness Program – Insight Perspective” organized by SSN College of Engineering on 3rd March 2017.
- 5) Attended Faculty Workshop on “Real time signal & image processing using Xilinx VIVADO system generator and simulation of Physical layer in communication” organized by St. Joseph’s Institute of Technology on 23rd 24th September, 2016.
- 6) Attended one day workshop on “Audio and Image Processing Using MATLAB” held at Sri Venkateswara College of Engineering.
- 7) Attended one day workshop on “Real time applications on IoT using MATLAB” organized by JNN Institute of Engineering on 22nd February 2018.
- 8) Attending “One day workshop on VLSI Design Tools” conducted by Sri venkateswara college of Engineering on 10th March 2017.
- 9) Attending “Two days’ workshop on FPGA based system design for Image processing algorithms” conducted by Sri venkateswara college of Engineering on 1st and 2nd February 2017.
- 10) Attending Online Workshop on “LaTeX” Conducting by IIT Bombay, from 14-17 May 2020.
- 11) Contributed Two Day’s Online National Level Workshop on “Machine Learning in Computer Vision Applications” Organized by ECE Department, SRI Venkateswara College of Engineering on 12th and 13th of May 2020.
- 12) National level workshop and hands-on-training on DIGITAL IC DESIGN USING CADENCE EDA , Vel Tech Multi Tech Engineering College.
- 13) Participated “Two days’ workshop on Analog custom IC design using Cadence EDA” held on 19th & 20th August, 2016 in Sri Venkateswara College of Engineering, Sriperumbudur.
- 14) Participated and successfully completed one-week workshop on “Outcome Based Education” from 14th – 18th June 2021 at National Institute of Technology, Tiruchirappalli.
- 15) Participated and successfully completed three days’ workshop on "Open-Source EDA Tools" from 03-12-2020 to 05-12-2020 at MEPCO Schlenk Schlenk Engineering College College SIVAKASI.
- 16) Participated and successfully completed two days’ workshop on "Modeling of SoC using Xilinx Vivado Tools" from 18-12-2020 to 19-12-2020 at PSG College of Technology, Coimbatore.
- 17) Participated one-day workshop on “System modeling, simulation and implementation using MATLAB and

OrCAD” organized by Sri Venkateswara College of Engineering on 13-14 March 2017.

- 18) Participated three days’ workshop on “Hands-on Training on virtual instrumentation, VLSI and Embedded Programming” organized by ECE Department, Vel Tech High Tech Engineering College, Chennai 7-9 December 2018.
- 19) Participated three days’ workshop on “Implementation of Image Processing Techniques Using OPENCV- Research Perspective” organized by ECE Department, Sri Venkateswara College of Engineering on 29 April 2019.
- 20) Workshop programme on ROBOTICS-ROBO TOUCH, ABLab Solutions.

Webinar (9)

- 1) Participated in the Webinar for “Full custom and semi-custom design flow” held on 4th August 2021 in association with CoreEL Technologies and Mentor.
- 2) Participated in the Webinar for “Autonomous Ocean Observation System Newer opportunities for Engineers” held on 31st October 2020 in association with Pals theory to practice session.
- 3) Actively Participated in the Webinar on “Human Resource Development” Organized by TNSCST Govt of Tamil Nadu, India on 6th October, 2021.
- 4) Actively Participated in the Webinar on “Semiconductor Device Modeling using Silvaco TCAD” Organized by Cognitive Design Technology Pvt Ltd, Bangalore, India on 7th May, 2021, in association with Silvaco
- 5) Actively Participated in the Webinar on “Data Visualization using Tableau” Organized by the Department of IT in association with BrainSwig on 15th June 2020.
- 6) Take a part in a seminar on “Neural Network and Deep Learning Using MATLAB” Conducted by St. Josephs College of Engineering on 20th June 2020 at Sriperumbudur, Chennai.
- 7) Participated in the Webinar for “Awareness on Innovation Voucher Program” at University College of Engineering Nagercoil held on 20.06.2020
- 8) Participated in the Eminent Faculty Lecture on “Signal & Biomedical Applications Based on Deep Learning” conducted by the Electronics department, Dayananada Sagar University, Bengaluru during Sep, 9 2020.
- 9) Attending in the webinar on "Semiconductor Device Modeling Using Silvaco TCAD" organised by Cognitive Design Technology Pvt Ltd, Bangalore, India on 7th May, 2021, in association with Silvaco.

Training Program (5)

- 1) In-plant training in BHARAT SANCHAR NIGAM LIMITED, Madurai.
- 2) In-plant training in PRASAR BHARATI Broadcast Corporation of India, Tirunelveli.
- 3) Add-on course on ADVANCED DIGITAL SYSTEM DESIGN USING HARDWARE DISCRPTION LANGUAGE, Karunya University.
- 4) Participated Online Certification Skill Development Program on “Digital System Design with Verilog and Vivado” held from 11th May to 15th May 2020, by Sandeepani School of Embedded System Design, Bangalore.
- 5) Participated in STTP on "Cyber Security Tools" held from 30th March to 3rd April 2021 conducted by Department of Information Technology, SSN, Chennai.
- 6) Participated in STTP on "Future Opportunities and Challenges in 5G Technology" held from October 13-17, 2021 conducted by IEEE Student Chapter of Madanapalle Institute of Technology & Science (MITS), Madanapalle.
- 7) Attended the Virtual Industry visit by L & T, HYDROCARBON DIVISION, VADODARA conducted by PALs on 16th SEPTEMBER 2020.
- 8) Participated in Online training on "Full custom and semi-custom design flow in VLSI using Cadence Tools " held on 29th & 30th April, 2021 conducted by B S Abdur Rahman Crescent Institute of Science & Technology and Entuple Technologies, Bangalore.

Webinar Organized (16)

- 1) Dr. S. Esther Florence, Associate Professor, SSN College of Engineering, Chennai was delivered a webinar on the title "EM Interference and Compatibility Studies due to On-body Antennas and Sensors" on 27-02-2021.
- 2) Ms. Rafridevi and Ms. Bhuvaneshwari (Alumni), Mr. Cooper Company, Chennai were delivered the webinar title "Success Behind the Toastmasters Club " on 13-03-2021.
- 3) Dr.Prabhu Chandhar, director of Chandhar Research Labs Pvt Ltd, Chennai, was the resource person for the webinar "5G cellular and drone communication systems" on 13-03-2021.
- 4) Webinar on "Staying Motivated to study during COVID-19" handled by Behavioral Specialist & Founder-Director, Learning Advantage, Kanchipuram on 08.05.2021.
- 5) Miss. Kalaiyarasi K, Project Manager, Tekiknow Technologies, Chennai, delivered a webinar on the title "Anechoic Chamber Measurements- An Overview" on 05.06.2021.
- 6) Miss. Vishwa and Sivaranjani, Mentee students, MAMCET, delivered a webinar on the title "Propel your career with technology" on 12.06.2021.
- 7) Dr. M.Lakshmanan, Professor, Galgotias College of Engineering and Technology, Greater Noida, delivered a webinar on the title "Inauguration of IETE and ECASTLE With a Spark of Wireless Communication" on 21.08.2021.
- 8) Dr. Narasimhan.K, Professor, SASTRA Deemed University,Thanjavur, delivered a webinar on the title "Challenges and Recent Trends in Signal Processing and Machine Learning" on 28.08.2021.
- 9) Dr. Sanjeev yadav, Professor, Government Women Engineering College, Ajmer, delivered a webinar on the title "Antennas and Microwave Engineering" on 09.09.2021.
- 10) Dr. Lalitha, Professor, Jain (Deemed-to-be-University), Bangalore delivered a webinar on the title "Artificial Intelligence and Robotics" on 11.09.2021.
- 11) Vinnalan K.A, Application engineer, CoreEL Technologies, Bangalore, delivered a webinar on the title "Mentor Graphics Full custom and Semi-custom design flow" on 18.09.2021.
- 12) Er. Prasanna Kumar, Product Manager at ABB India Limited, Bangalore, delivered a webinar on the title "Inauguration of i- COMM club and webinar on Recent trends of Information and Communication technologies" on 23.09.2021.
- 13) Prof. Jency Rubia J, Assistant Professor, Department of ECE, M.A.M College of Engineering and Technology, Trichy, delivered a webinar on the title "Patent Processing and Strategies" on 02.10.2021.
- 14) Ms. S.Devi Poonguzhali/AP/ECE, M.A.M College of Engineering and Technology, Trichy has coordinate the workshop on the title "XILINX WORKSHOP CUM CODING CONTEST ON VERILOG CODING" on 25.10.2021.
- 15) Ms. A Kaviya/AP/ECE, M.A.M College of Engineering and Technology, Trichy has coordinated the Competition on the title "VHDL/Verilog Coding War" on 26.10.2021.
- 16) Ms Srenedhi, System Engineer, Infosys (Mysore Campus) delivered a webinar on the title "Design for Internet of Things" on 28.10.2021.

Delivered Invited Lectures (1)

- 1) Prof. Jency Rubia J, Assistant Professor, Department of ECE, M.A.M College of Engineering and Technology, Trichy, delivered a webinar on the title "Patent Processing and Strategies" on 02.10.2021.

PROFESSIONAL RESPONSIBILITY

Member of Technical Societies (9)

- 1) Member of ACM Digital Library with Client No: 0194767
- 2) Associate Member of IETE Society with Membership number: AM500226
- 3) Life Member of ISTE with Membership number: LM132055

- 4) Member of IAENG journal with Membership Number: 176590
- 5) Associate Member of UACEE-IREC with Membership number: AM101000582370
- 6) Member of SCIEI Conference with Membership Number: 20191218002
- 7) Member of SDIWC with Membership Number ID:27676
- 8) Member of International Association of Online Engineering (IAOE) with Membership No:11133
- 9) Global Member of Internet Society with Number: 2214703

International Technical Committee (6)

1. International Conference on Computer Modeling, Simulation and Algorithm (CMSA 2020), Beijing, China.
2. International Conference on Electrical, Automation and Mechanical Engineering (EAME 2020), Beijing, China.
3. International Conference on Bioengineering, Clinical Medicine and Artificial Intelligence (BCMAI-2020), Beijing, China.
4. 2020 International Workshop on Intelligent Computing, Communication and Data Engineering (ICCDE2020) Beijing, China.
5. 2020 3rd International Conference on Modeling, Simulation and Optimization Technologies and Applications (M SOTA2020) Beijing, China.
6. The 10th International Conference on Electronics, Communications and Networks (CECNet 2020)

Editorial Board Member (1)

1. American Journal of Electrical and Computer Engineering

Peer-Reviewer (3)

1. Measurement and Control (SAGE)
2. International Journal of Advanced Robotic Systems (SAGE)
3. International Journal of Natural Computing Research

CLUB (1)

Member of Toastmasters International Number: 7814687

i-COMM Technovation Club In-Charge